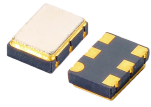


### C7VC HCMOS SURFACE MOUNT VOLTAGE CONTROLLED CRYSTAL CLOCK OSCILLATOR

**7.0 x 5.0 x 1.8 mm**

**APPLICATIONS:**

 Set-top box (STB), Modems  
 Communication Equipments  
 Digital TV, Computers

**FEATURES:**

 Low Jitter, Tristate Function  
 Available from supply voltage 3.3 to 5.0V  
 Available for -40°C to 85°C industrial application  
 Pb free/ RoHS Compliant

**PART NUMBERING GUIDE**
**C7VC — ..... — ..... — ..... — ..... — ..... — .....**

Frequency (MHz)	Supply Voltage 3.3= +3.3V 5.0= +5.0V	Freq. Stability S2: ± 25ppm S3: ± 30ppm S4: ± 50ppm S5: ± 100ppm	Operating Temp. A1: -10°C to +60°C A2: -10°C to +70°C A3: -20°C to +70°C A4: -20°C to +85°C A5: -30°C to +85°C A6: -40°C to +85°C	Symmetry Blank= 40/60% 45 = 45/55% (@ 1/2 Vdd)	Pullability P50= 50ppm min. P100= 100ppm min.
--------------------	--	--	---	---	---

**ELECTRICAL CHARACTERISTICS**

PARAMETERS	SPECIFICATION
Frequency Range	1.000 ~ 80.000 MHz
Frequency Stability	± 25 ppm ~ ± 100 ppm
Supply Voltage(Vdd)	+3.3V or +5.0V ±10%
Control Voltage Range(Vc)	+1.65±1.35V or +2.5V± 2.0V
Input Curent	20 mA max.
Operating Temperature	- 10 to + 60°C ~ -40 to +85°C
Storage Temperature	- 40°C to + 85°C ~ - 55°C to + 125°C
Symmetry (@ 1/2Vdd)	40/60% (Standard) or 45/55%
Linearity	10% max.
Pullability	± 50 ppm ~ ± 100 ppm min.
Phase Noise	-140 dBc / Hz @1KHz
RMS Jitter (12KHz ~ 20MHz)	1 ps max.
Output Load	CMOS 15pF
Output Level	VOH: ≥ 0.9*Vdd      VOL: ≤ 0.1*Vdd
Rise Time/ Fall Time (Tr/Tf)	6 nS Max.
Tri-state Function	PIN# 2 (High or Open) ==> PIN# 4: Oscillation PIN# 2 (Low) ==> PIN# 4: High Impedance

**OUTLINE DRAWING: mm**
**RECOMMENDED SOLDER PAD LAYOUT**

PIN	FUNCTION
1	Voltage control
2	Tristate or N/C
3	Ground
4	Output
5	N/C or Tristate
6	Vdd

If you require further assistance, please feel free contact us at antonio@asiastek.com

**ASIAS TECHNOLOGY CO.,LTD.** [www.asiastek.com](http://www.asiastek.com)

Tel: +886 02- 2943 9330