





AX32 SEAM SEALED CERAMIC SMD CRYSTAL

3.2 x 2.5 x 0.8 mm



APPLICATIONS:

Wireless Applications Bluetooth, Computers Mobile, Communication

FEATURES:

Seam Sealed Ultra Small Ceramic SMD Package AT-Cut Fundamental Tight Tolerence and Stability Available Pb free/ RoHS Compliant

PART NUMBERING GUIDE

AX32 —							
	Frequency	Oscillation Mode	Load Capacitance	Freq. Tolerance	Freq. Stability	Operating Temp.	ESR
	(MHz)	F: Fundamental	Please specify (CL)value in pf. SR for series.	10: ± 10ppm 15: ± 15ppm 20: ± 20ppm 25: ± 25ppm 30: ± 30ppm	10: ± 10ppm 15: ± 15ppm 20: ± 20ppm 25: ± 25ppm 30: ± 30ppm	A1: -10°C to +60°C A2: -10°C to +70°C A3: -20°C to +70°C A4: -20°C to +85°C A5: -30°C to +85°C	Please refer to below ESR table.
				30: ± 30ppm 50: ± 50ppm	30: ± 30ppm 50: ± 50ppm	A5: -30°C to +85°C A6: -40°C to +85°C	

ELECTRICAL CHARACTERISTICS

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PARAMETERS	SYMBOL	SPECIFICATION				
Frequency Range	Fo	12.000 ~ 54.000 MHz				
Operation Mode		Fundamental(AT Cut)				
Operating Temperature	То	- 20 to + 70°C, -40 to +85°C, or specify				
Storage Temperature		- 40°C to + 85°C				
Frequency Tolerance at +25°C	∆f/F	\pm 10 ppm, \pm 15 ppm, \pm 30 ppm or specify				
Frequency Stability Over	^ £ /□	± 10 ppm, ± 15 ppm, ± 30 ppm or specify				
Operating Temperature Range	△f/F					
Equivalent Series Resistance	R1	Please find below Table				
Shunt Capacitance	C0	7pF max.				
Load Capacitance	CL	series, 18pF(Standard), or specify				
Drive Level	DL	300μW max., 100μW typical				
Aging at 25°C	△f/F	± 3ppm max.(first year)				
Insulation Resistance		500MΩ min at 100Vdc				

EQUIVALENT SERIES RESISTANCE(ESR)Table OUTLINE DRAWING: mm

	201711102(2011)14210	OC: ZZIIIZ DIGITIZZII
Frequency Range(MHz)	ESR(Ω) max.	3.2±0.15
Fundai	mental	4 3
12.000 ~ 13.999	150	15
14.000 ~ 15.999	100	7 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2
16.000 ~ 19.999	80	
20.000 ~ 29.999	60	- I WAX
30.000 ~ 54.000	50	· ·
		-0.1
		0.9
		*Note: From the top view, pad #1 is always located at the bottom left corner of the package.
		Due to material supply availability, the chamfer could be located on pad#1, 2 or 4.
		Please be advised that the chamfer location has no impact on the electrical performance of the unit.

If you require further assistance, please feel free contact us at antonio@asiastek.com

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